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Cox where Q_1 is the additional charge added just below the oxide-semiconductor surface by ion implantation. COX is a parallel-plate capacitance per unit area ϵ_s given by $C_{ox} = \epsilon_s / d$ (d is the oxide thickness, ϵ_s is the permittivity of the semiconductor) $d = 1V \times 3.9 \times 8.85 \times 10^{-14} \text{ F/cm} \dots$

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3. 1 CHAPTER 2 1. (a) From Fig. 11a, the atom at the center of the cube is surrounded by four equidistant nearest neighbors that lie at the corners of a tetrahedron. Therefore the distance between nearest neighbors in silicon ($a = 5.43 \text{ \AA}$) is $1/2 [(a/2)^2 + (a/2)^2]^{1/2} = a/\sqrt{2} = 2.35 \text{ \AA}$.

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